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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re: Application of: David G. Bellemore, et al Attorney Docket: 712-002.107

Serial No. 09/929,423

Examiner: Antione Royall

Filed: August 14, 2001

Group Art Unit: 2121



**For: METHOD FOR REDUCING SKEW IN A REAL-TIME
CENTROID CALCULATION**

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**ASSISTANT COMMISSIONER OF PATENTS
WASHINGTON, D.C. 20231**

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Further to an Information Disclosure Statement filed on November 12, 2002, applicants submit herewith references of which they are aware, which they believe may be material to the examination of this application and in respect of which they may have a duty to disclose in accordance with 37 C.F.R. 1.56.

This Supplemental Information Disclosure Statement is timely since it is certified by the undersigned that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office (copy enclosed) in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement per 37 C.F.R. 1.97(c) and 37 C.F.R. 1.97 (e)(1).

I hereby certify that this correspondence is being deposited today
with the United States Postal Service as first class mail in an
envelope addressed to Assistant Commissioner of Patents, Washington, D.C. 20231

Jodie Droniak
Jodie Droniak

Dec. 27, 2002
Date

Serial No.09/929,423
Atty. Docket No. 712-002.107

The references are listed in the enclosed foreign search report dated December 2, 2002.

A concise explanation of the relevance of each reference is specifically pointed out on page 1-
2 of the search report. A PTO 1449 listing the cited references is also enclosed.

Respectfully submitted,

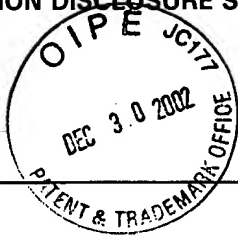


James A. Retter

Registration No. 41,266

Date: Dec. 27, 2002

WARE, FRESSOLA, VAN DER SLUYS
& ADOLPHSON LLP
Bradford Green, Building 5
755 Main Street, PO Box 224
Monroe, CT 06468
(203) 261-1234

FORM PTO-1449
INFORMATION DISCLOSURE STATEMENT

 ATTY DOCKET NO.
 712-002.107

 SERIAL NO.
 09/929,423

APPLICANTS: David G. Bellemore, et al

 FILING DATE:
 August 14, 2001

ART UNIT: 2121

UNITED STATES PATENT DOCUMENTS

EXAM. INITIAL		DOCUMENT NUMBER	DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	US	6 233 373 B1	5/15/2001	Putnam, Martin et al			
	US	6 115 675 A	9/4/2000	Benco, Joseph et al			
	US	2001/0011289 A1	8/2/2001	Bellemore, et al			
	US	6 201 909 B1	3/13/2001	Rakuljic, George et al			
	US	5 825 670 A	10/20/1998	Chernoff, D. et al			

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	INVENTOR/ASSIGNEE	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

		Patent Abstracts of Japan, Vol. 011, No. 245; August 11, 1987; and JP 62 047316 A (Mitsubishi Electric Corp.), March 13, 1987.

Examiner

Date: